

Amendments to the Specification:

Please replace the Abstract, on page 37, lines 1 - 16, with the following :

~~-Method and Arrangement for Arithmetic Encoding and Decoding Binary States and a Corresponding Computer Program and a Corresponding Computer-readable Storage Medium~~

ABSTRACT

A method and arrangement for arithmetic encoding/decoding is described, wherein the probability estimation is performed by a finite state machine FSM, wherein the generation of N representative states of the FSM is performed offline. Corresponding transition rules are filed in the form of tables. In addition, a pre-quantization of the interval width R to a number of K pre-defined quantization values is carried out. With suitable dimensioning of K and N, this allows the generation of a table containing all K x N combinations of pre-calculated product values $R \times P_{LPS}$ for a multiplication-free determination of R_{LPS} . Overall, the result is a good compromise between high coding efficiency and low calculation effort. ~~The invention describes a method and an arrangement for an arithmetic encoding and decoding of binary states and a corresponding computer program and a corresponding computer-readable storage medium, which may in particular be used in digital data compression.~~

~~To this end it is proposed to for example perform a table-aided binary arithmetic encoding and decoding using two or several tables. --~~